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S/N Unknown

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Robert D. Norman	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	703.070US2
Title:	SYSTEM AND METHOD FOR ASSIGNING ADDRESSES TO MEMORY DEVICES		

**PRELIMINARY AMENDMENT**

Box Patent Application  
Commissioner for Patents  
Washington, D.C. 20231

Before taking up the above-identified application for examination, please enter the following amendments.

**IN THE SPECIFICATION**

On page 1, under the title, please insert ✓ This application is a Continuation of U.S.

a Serial No. 08/842030 filed on April 23, 1997.--

**IN THE CLAIMS**

✓ Please cancel claims 4-24, 26-46, 48-54, 60, 62, and 63. Claims 1-3, 25, 47, 55-59 and 61 are now pending in this application.

1. (Once amended) A memory system comprising:

a plurality of memory devices, with each memory device comprising

- (a) an array of memory cells;
- (b) an addressing circuitry [adapted to address] operatively coupled to the array of the memory cells, wherein the addressing circuitry is capable of providing addresses to the array of memory cells;
- (c) a memory device bus interface;
- (d) a command decoder which decodes commands at the memory device bus interface, including an address assign command; and
- (e) a local address storage circuitry which stores a local address for identifying the storage circuitry's single [the] associated memory device once the address assign command is decoded by the command decoder;

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